Date: 02/08/2017

Circular

Question Paper Scrutiny Committee

The scrutiny of question papers of IA is scheduled as below

Sl.No.	IA	Date of scrutiny
1.	1st	15/09/2017
2.	2nd	14/10/2017
3.	3rd	22/11/2017

All the chairman and members of QPSC are hereby informed to scrutinize question papers of IA as per the above schedule. Further all the faculty members are informed to submit the draft question papers (soft copy) to the respective QPSC chairman one day before the schedule.

Head of the Department
HOD. E&C
Electronics and Communication
JNNCE, SHIMOGA-577 204

1. SVS
2. PM
3. GCK
4. SMD
5. ABP
6. BSU
7. SBN
8. SSM
9. AN
10. KVD
11. AKJ
12. SS
13. RBS
14. PSC
15. SK

19. PGS
20. SB
21. SPM
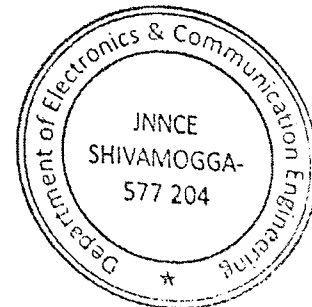
JAWAHARLAL NEHRU NATIONAL COLLEGE OF ENGINEERING, SHIMOGA

Department of Electronics and Communication Engineering

Question Paper Scrutiny Committee

Date:02/09/2017

The following faculty members are nominated as the members of question paper scrutiny committee

Year Program:2017-18 (Odd)/ E&C

Domain Stream: Fundamental courses/ Interdisciplinary

Courses: Basic Electronics- I/II sem; Analog Electronics, Network Analysis, Electronic Instrumentation, Engineering Electromagnetics - III sem; Management and Entrepreneurship Development - V sem; Power Electronics- VIIsem.

Members:

Sl. No.	Name	Designation	Role	Signature
1.	G C Kamalamma	Asst. Professor	Chairman	G.C. Kamalamma
2.	Sunil M D	Asst. Professor	Member	
3.	Nalina S B	Asst. Professor	Member	
4.	Ujwala B S	Asst. Professor	Member	

HOD/E&C Department
Head of the Department
Electronics and Communication
JNNCE, SHIMOGA-577 204

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577 204

7.		Power Electronics	COs of choice questions were not same
8.	Communication and signal processing	Information Theory and Coding	Marks were not as per the proportionate
9.		Digital Signal Processing	Nil
10.		Computer Communication Networks	Nil
11.		Optical Fiber Communication	COs of choice questions were not same
12.	Embedded, VLSI and Programming	Digital Electronics	BTL were not mentioned
13.		Verilog HDL	Nil
14.		Operating Systems	Scheme was not uniform
15.		Object Oriented Programming using C++	Marks were not as per the proportionate
16.		Embedded System Design	Marks were not as per the proportionate

All the faculty members whose question papers were not as per the guidelines / with discrepancies were called and one to one suggestions were made and changes were made there itself. Finally, the question papers were approved.

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Chairman/Member
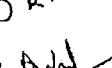Question Paper Scrutiny Committee
Fundamental Courses / Interdisciplinary
Dept. of E & C, JNNCE, Shivamogga.

Chairman/Member
Question Paper Scrutiny Committee
Communication & Signal Processing Stream
Dept. of E & C, JNNCE, Shivamogga.

Chairman/Member
Question Paper Scrutiny Committee
Embedded, VLSI & Programming Stream
Dept. of E & C, JNNCE, Shivamogga.

JAWAHARLAL NEHRU NATIONAL COLLEGE OF ENGINEERING, SHIMOGA

Department of Electronics and Communication Engineering

Question Paper Scrutiny Committee

Date:02/09/2017

The following faculty members are nominated as the members of question paper scrutiny committee

Year/ Program:2017-18(Odd)/ E&C

Domain Stream: Communication and signal processing

Courses: Information Theory and Coding, Digital Signal Processing-V sem; Computer Communication Networks, Optical Fiber Communication- VII sem.

Members:

Sl. No.	Name	Designation	Role	Signature
1.	Dr. P Manjunatha	Professor	Chairman	
2.	Ajay Betur P	Asst. Professor	Member	
3.	Darshan K V	Asst. Professor	Member	K.V. Darshan
4.	Sharath S M	Asst. Professor	Member	

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577 204

JAWAHARLAL NEHRU NATIONAL COLLEGE OF ENGINEERING, SHIMOGA

Department of Electronics and Communication Engineering

Question Paper Scrutiny Committee

Date:02/09/2017

The following faculty members are nominated as the members of question paper scrutiny committee

Year/ Program:2017-18(Odd)/ E&C

Domain/ Stream: Embedded, VLSI and Programming

Courses: Digital Electronics-III sem; Verilog HDL, Operating Systems, Object Oriented Programming using C++-V sem; Embedded System Design- VII sem.

Members:

Sl. No.	Name	Designation	Role	Signature
1.	Dr. S V Sathyanarayana	Professor	Chairman	
2.	Sheela S	Asst. Professor	Member	
3.	Pradeepa S C	Asst. Professor	Member	
4.	Abhijith N	Asst. Professor	Member	

HOD/E&C

Head of the Department
Electronics and Communication
JNNCE, SHIMOGA-577 204

National Education Society (R.)

Jawaharlal Nehru National College of Engineering, Shivamogga
(Approved by AICTE, New Delhi, Recognized by Govt. of Karnataka and Affiliated to VTU, Belagavi)

Department of Electronics& Communication Engineering

Date: 15/09/2017

Proceedings of first QPSC meeting held on 15/09/2017 in Communication Laboratory.

Members Present:

Sl. No.	Name	Designation	Role	Signature
Domain/ Stream: Fundamental courses/ Interdisciplinary				
1.	G C Kamalamma	Assoc. Professor	Chairman	
2.	Sunil M D	Asst. Professor	Member	
3.	Nalina S B	Asst. Professor	Member	
4.	Ujwala B S	Asst. Professor	Member	
Domain/ Stream: Communication and signal processing				
5.	Dr. P Manjunatha	Professor	Chairman	
6.	Ajay Betur P	Asst. Professor	Member	
7.	Darshan K V	Asst. Professor	Member	
8.	Sharath S M	Asst. Professor	Member	
Domain/ Stream: Embedded, VLSI and Programming				
9.	Dr. S V Sathyanarayana	Professor	Chairman	
10.	Sheela S	Asst. Professor	Member	
11.	Pradeepa S C	Asst. Professor	Member	
12.	Abhijith N	Asst. Professor	Member	

Agenda:

* Scrutiny of first IA question papers

Proceedings:

The question papers and the scheme and solutions were scrutinized under the following domains / streams.

Sl.No.	Domain	Course	Suggestions / Discrepancies found
1.	Fundamental courses/ Interdisciplinary	Basic Electronics	COs were not mapped properly
2.		Network Theory	COs of choice questions were not same
3.		Engineering Electromagnetics	Nil
4.		Analog Electronics	BTL were not mentioned
5.		Electronics Instrumentation	Nil
6.		Management and Entrepreneurship Development	Scheme was not uniform

9.		Computer Communication Networks	Marks were not as per the proportionate
10.		Wireless Communication	BTL were not mentioned
11.	Embedded, VLSI and Programming	Microprocessor	COs were not mapped properly
12.		ARM Microcontroller and Embedded Systems	Nil
13.		VLSI Design	COs of choice questions were not same
14.		Data Structures using C++	Scheme was not uniform

All the faculty members whose question papers were not as per the guidelines / with discrepancies were called and one to one suggestions were made and changes were made there itself. Finally, the question papers were approved.

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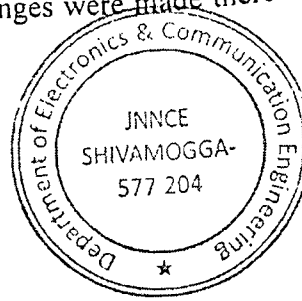
Chairman/Member
Question Paper Scrutiny Committee
Fundamental Courses / Interdisciplinary
Dept. of E & C, JNNCE, Shivamogga.

Chairman/Member
Question Paper Scrutiny Committee
Communication & Signal Processing Stream
Dept. of E & C, JNNCE, Shivamogga.

Chairman/Member
Question Paper Scrutiny Committee
Embedded, VLSI & Programming Stream
Dept. of E & C, JNNCE, Shivamogga.

Date:21/04/2018

Proceedings of first QPSC meeting held on 21/04/2018 in Communication Laboratory.

Members Present:

Sl. No.	Name	Designation	Role	Signature
Domain/ Stream: Fundamental courses/ Interdisciplinary				
1.	G C Kamalamma	Assoc. Professor	Chairman	
2.	Sunil M D	Asst. Professor	Member	
3.	Nalina S B	Asst. Professor	Member	
4.	Ujwala B S	Asst. Professor	Member	
Domain/ Stream: Communication and signal processing				
5.	Dr. P Manjunatha	Professor	Chairman	
6.	Ajay Betur P	Asst. Professor	Member	
7.	Darshan K V	Asst. Professor	Member	
8.	Sharath S M	Asst. Professor	Member	
Domain/ Stream: Embedded, VLSI and Programming				
9.	Dr. S V Sathyanarayana	Professor	Chairman	
10.	Sheela S	Asst. Professor	Member	
11.	Pradeepa S C	Asst. Professor	Member	
12.	Abhijith N	Asst. Professor	Member	

Agenda:

- Scrutiny of second IA question papers

Proceedings:

The question papers and the scheme & solutions were scrutinized under the following domains / streams.

Sl.No.	Domain	Course	Suggestions / Discrepancies found
1.	Fundamental courses/ Interdisciplinary	Basic Electronics	COs of choice questions were not same
2.		Control Systems	Nil
3.		Linear Integrated Circuits	COs were not mapped properly
4.		Digital Switching Systems	Nil
5.		Digital Switching Systems	Nil
6.	Communication and signal processing	Signals and Systems	Nil
7.		Principals of Communication Systems	Scheme was not uniform
8.		Digital Communication	BTL were not mentioned

		Computer Communication Networks	Marks were not as per the proportionate
	Unsupported Personality: PCL 9.		
10.		Wireless Communication	BTL were not mentioned
11.	Embedded, VLSI and Programming	Microprocessor	COs were not mapped properly
12.		ARM Microcontroller and Embedded Systems	COs of choice questions were not same
13.		VLSI Design	Scheme was not uniform
14.		Data Structures using C++	COs of choice questions were not same
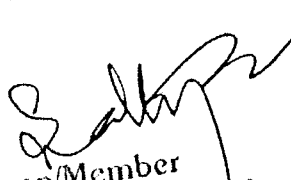
All the faculty members whose question papers were not as per the guidelines / with discrepancies were called and one to one suggestions were made and changes were made there itself. Finally, the question papers were approved.

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[seal: Department of Electronics & Communication Engineering, JNNCE SHIVAMOGGA-577 204]

Chairman/Member
Question Paper Scrutiny Committee
Fundamental Courses / Interdisciplinary
Dept. of E & C, JNNCE, Shivamogga.

Chairman/Member
Question Paper Scrutiny Committee
Communication & Si~ · ..ing Stream
Dept. of E & C, '.

Chairman/Member
Question Paper Scrutiny Committee
Embedded, VLSI & Programming Stream
Dept. of E & C, JNNCE, Shivamogga.
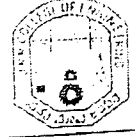
Date:19/05/2018

Proceedings of first QPSC meeting held on 19/05/2018 in Communication Laboratory.

Members Present:

Sl. No.	Name	Designation	Role	Signature
Domain/ Stream: Fundamental courses/ Interdisciplinary				
1.	G C Kamalamma	Assoc. Professor	Chairman	G.C Kamalamma
2.	Sunil M D	Asst. Professor	Member	
3.	Nalina S B	Asst. Professor	Member	
4.	Ujwala B S	Asst. Professor	Member	
Domain/ Stream: Communication and signal processing				
5.	Dr. P Manjunatha	Professor	Chairman	
6.	Ajay Betur P	Asst. Professor	Member	
7.	Darshan K V	Asst. Professor	Member	
8.	Sharath S M	Asst. Professor	Member	
Domain/ Stream: Embedded, VLSI and Programming				
9.	Dr. S V Sathyanarayana	Professor	Chairman	
10.	Sheela S	Asst. Professor	Member	
11.	Pradeepa S C	Asst. Professor	Member	
12.	Abhijith N	Asst. Professor	Member	

Agenda:
- Scrutiny of third IA question papers

Proceedings:

The question papers and the scheme & solutions were scrutinized under the following domains / streams.

| Sl.No.	Domain	Course	Suggestions / Discrepancies found
1.	Fundamental courses/ Interdisciplinary	Basic Electronics	Nil
2.		Control Systems	Nil
3.		Linear Integrated Circuits	COs were not mapped properly
4.		Digital Switching Systems	Nil
5.		Digital Switching Systems	COs of choice questions were not same
6.	Communication and signal processing	Signals and Systems	Scheme was not uniform
7.		Principals of Communication Systems	BTL were not mentioned
8.		Digital Communication	Nil

9.	Communication and signal processing	Information Theory and Coding	Nil
10.		Digital Signal Processing	Nil
12.		Microwaves and Antennas	Nil
		Digital Image Processing	Nil
13.		Multimedia Communication	Nil
14.	Embedded, VLSI and Programming	Digital Electronics	Nil
15.		Verilog HDL	Nil
16.		Operating Systems	Marks were not as per the proportionate
17.		Object Oriented Programming using C++	Nil
18.		IoT and Wireless Sensor Networks	Nil

All the faculty members whose question papers were not as per the guidelines / with discrepancies were called and one to one suggestions were made and changes were made there itself. Finally, the question papers were approved.

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Chairman/Member
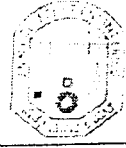Question Paper Scrutiny Committee
Fundamental Courses / Interdisciplinary
Dept. of E & C, JNNCE, Shivamogga.

Chairman/Member
Question Paper Scrutiny Committee
Communication & Signal Processing Stream
Dept. of E & C, JNNCE, Shivamogga.

Chairman/Member
Question Paper Scrutiny Committee
Embedded, VLSI & Programming Stream
Dept. of E & C, JNNCE, Shivamogga.

National Education Society (R.)

Jawaharlal Nehru National College of Engineering, Shivamogga
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Department of Electronics& Communication Engineering

Date:20/11/2018

Proceedings of first QPSC meeting held on 20/11/2018 in Communication Laboratory.

Members Present:

Sl. No.	Name	Designation	Role	Signature
	Domain/ Stream: Fundamental courses/ Interdisciplinary			
1.	G C Kamalamma	Assoc. Professor	Chairman	G C Kamalamma .
2.	Sunil M D	Asst. Professor	Member	
3.	Nalina S B	Asst. Professor	Member	
4.	Ujwala B S	Asst. Professor	Member	
	Domain/ Stream: Communication and signal processing			
5.	Dr. P Manjunatha	Professor	Chairman	
6.	Ajay Betur P	Asst. Professor	Member	
7.	Darshan K V	Asst. Professor	Member	KV
8.	Sharath S M	Asst. Professor	Member	
	Domain/ Stream: Embedded, VLSI and Programming			
9.	Dr. S V Sathyanarayana	Professor	Chairman	Sathy
10.	Sheela S	Asst. Professor	Member	
11.	Pradeepa S C	Asst. Professor	Member	
12.	Abhijith N	Asst. Professor	Member	

Agenda:
* Scrutiny of third IA question papers

Proceedings:

The question papers and the scheme and solutions were scrutinized under the following domains / streams.

Sl.No.	Domain	Course	Suggestions / Discrepancies found
1.	Fundamental courses/ Interdisciplinary	Basic Electronics	Nil
2.		Network Theory	Nil
3.		Engineering Electromagnetics	COs were not mapped properly
4.		Analog Electronics	Nil
5.		Electronics Instrumentation	Nil
6.		Management and Entrepreneurship Development	Nil
7.		Power Electronics	Nil
8.		Signals and Systems	Nil

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VHDL QP for Scrutiny

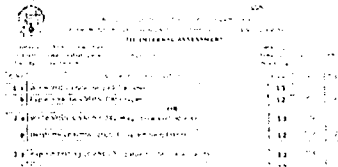**N Abhijith** <abhijithn@jnnce.ac.in>
to Sunil

Dear Sir
PFA

Abhijith N
Asst.Prof
Dept. of Electronics and Communication Engineering
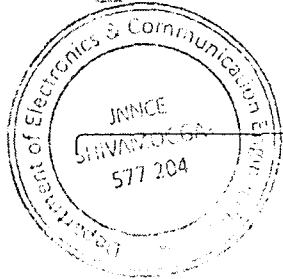J.N.N College of Engineering
Shimoga-577204
Ph.No-9620852497

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7.		Power Electronics	Scheme was not uniform
8.	Communication and signal processing	Digital Signal Processing	Nil
9.		Microwaves and Antennas	BTL were not mentioned
10.		Digital Image Processing	Nil
11.		Multimedia Communication	Nil
12.	Embedded, VLSI and Programming	Digital System Design	Nil
13.		Computer Organization & Architecture	Nil
14.		Verilog HDL	BTL were not mentioned
15.		Operating Systems	
16.		Object Oriented Programming using C++	COs were not mapped properly
17.		IoT and Wireless Sensor Networks	Nil

All the faculty members whose question papers were not as per the guidelines / with discrepancies were called and one to one suggestions were made and changes were made there itself. Finally, the question papers were approved.

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[Seal: Electronics & Communication Engineering, Department of, JNNCE SHIVAMOGGA-577 204]
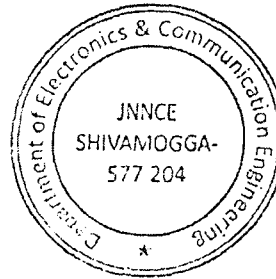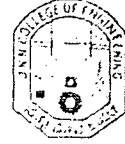
Chairman/Member
Question Paper Scrutiny Committee
Fundamental Courses / Interdisciplinary
Dept. of E & C, JNNCE, Shivamogga.

Chairman/Member
Question Paper Scrutiny Committee
Communication & Signal Processing Stream
Dept. of E & C, JNNCE, Shivamogga.

Chairman/Member
Question Paper Scrutiny Committee
Embedded, VLSI & Programming Stream
Dept. of E & C, JNNCE, Shivamogga.

Date:07/09/2019

Proceedings of first QPSC meeting held on 07/09/2019 in Communication Laboratory.

Members Present:

Sl. No.	Name	Designation	Role	Signature
Domain/ Stream: Fundamental courses/ Interdisciplinary				
1.	Dr. Pramod Kumar S	Assoc. Professor	Chairman	
2.	Sunil M D	Asst. Professor	Member	
3.	Nalina S B	Asst. Professor	Member	
4.	Ujwala B S	Asst. Professor	Member	
Domain/ Stream: Communication and signal processing				
5.	Dr. P Manjunatha	Professor	Chairman	
6.	Ajay Betur P	Asst. Professor	Member	
7.	Darshan K V	Asst. Professor	Member	
8.	Sharath S M	Asst. Professor	Member	
Domain/ Stream: Embedded, VLSI and Programming				
9.	Dr. S V Sathyanarayana	Professor	Chairman	
10.	Sheela S	Asst. Professor	Member	
11.	Pradeepa S C	Asst. Professor	Member	
12.	Abhijith N	Asst. Professor	Member	

Agenda:

- Scrutiny of first IA question papers

Proceedings:

The question papers and the scheme & solutions were scrutinized under the following domains / streams.

Sl.No.	Domain	Course	Suggestions / Discrepancies found
1.	Fundamental courses/ Interdisciplinary	Basic Electronics	COs were not mapped properly
2.		Network Theory	Nil
3.		Electronic Devices	COs of choice questions were not same
4.		Power Electronics & Instrumentation	Nil
5.		Management and Entrepreneurship Development	Marks were not as per the proportionate
6.		Information Theory and Coding	Nil

JAWAHARLAL NEHRU NATIONAL COLLEGE OF ENGINEERING, SHIMOGA

Department of Electronics and Communication Engineering

Question Paper Scrutiny Committee

Date:04/09/2019

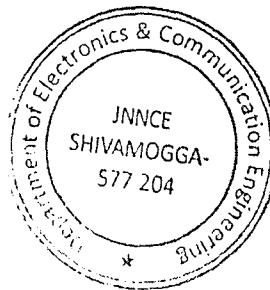The following faculty members are nominated as the members of question paper scrutiny committee

Year/ Program:2019-20 (Odd)/ E&C

Domain/ Stream: Embedded, VLSI and Programming

Courses: Digital System Design, Computer Organization &Architecture-III sem; Verilog HDL, Operating Systems, Object Oriented Programming using C++-V sem; IoT and Wireless Sensor Networks- VII sem.

Members:

Sl. No.	Name	Designation	Role	Signature
1.	Dr. S V Sathyanarayana	Professor	Chairman	
2.	Sheela S	Asst. Professor	Member	
3.	Pradeepa S C	Asst. Professor	Member	
4.	Abhijith N	Asst. Professor	Member	

HOD/E&C
Head of the department
Electronics and Communication
JNNCE, SHIMOGA-577 204

JAWAHARLAL NEHRU NATIONAL COLLEGE OF ENGINEERING, SHIMOGA

Department of Electronics and Communication Engineering

Question Paper Scrutiny Committee

Date:04/09/2019

The following faculty members are nominated as the members of question paper scrutiny committee
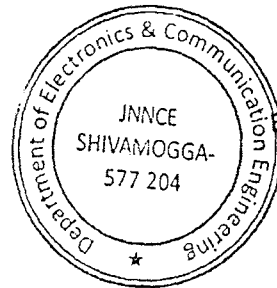
Year/ Program:2019-20 (Odd)/ E&C

Domain/ Stream: Communication and signal processing

Courses: Digital Signal Processing-V sem; Microwaves and Antennas, Digital Image Processing, Multimedia Communication- VII sem.

Members:

Sl. No.	Name	Designation	Role	Signature
1.	Dr. P Manjunatha	Professor	Chairman	
2.	Ajay Betur P	Asst. Professor	Member	
3.	Darshan K V	Asst. Professor	Member	K.V Darshan
4.	Sharath S M	Asst. Professor	Member	

HOD/E&C
Head of the Department
Electronics and Communication
JNNCE, SHIMOGA-577 204

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577 204

JAWAHARLAL NEHRU NATIONAL COLLEGE OF ENGINEERING, SHIMOGA

Department of Electronics and Communication Engineering

<u>Question Paper Scrutiny Committee</u>

Date:04/09/2019

The following faculty members are nominated as the members of question paper scrutiny committee

Year/ Program: 2019-20 (Odd)/ E&C

Domain/ Stream: Fundamental courses/ Interdisciplinary

Courses: Basic Electronics- I/II sem; Network Theory, Electronic Devices, Power Electronics &Instrumentation- III sem; Management and Entrepreneurship Development, Information Theory and Coding- V sem; Power Electronics- VIIsem.

Members:

Sl. No.	Name	Designation	Role	Signature
1.	Dr. Pramod Kumar S	Assoc. Professor	Chairman	
2.	Sunil M D	Asst. Professor	Member	
3.	Nalina S B	Asst. Professor	Member	
4.	Ujwala B S	Asst. Professor	Member	

HOD/E&C
Head of the Department
Electronics and Communication
JNNCE, SHIMOGA-577 204

Date: 02/08/2019

Circular

Question Paper Scrutiny Committee

The scrutiny of question papers of IA is scheduled as below

Sl.No.	IA	Date of scrutiny
1.	1st	07/09/2019
2.	2nd	16/10/2019
3.	3rd	22/11/2019

All the chairman and members of QPSC are hereby informed to scrutinize question papers of IA as per the above schedule. Further all the faculty members are informed to submit the draft question papers (soft copy) to the respective QPSC chairman one day before the schedule.

HOD, E&C
Head of the Department
Electronics and Communication
JNNCE, SHIMOGA-577 204

1. SVS
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15. SK
16. PKN
17. SM
18. SHR
19. PGS
20. SB

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National Education Society (R.)

J.N.N College of Engineering, Shivamogga

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All 7 UG Programs have been Accredited By National Board Of Accreditation (NBA)
for period 1.7.2019 to 30.6.2022,
Recognized by Govt. of Karnataka and Affiliated to VTU, Belagavi)

Department of Electronics and Communication Engineering

Question Paper Scrutiny Committee

Academic Year: 2020-21(Even)   Date: 25-05-2021
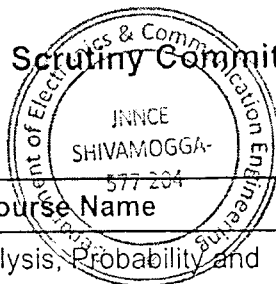
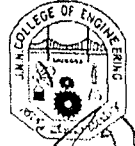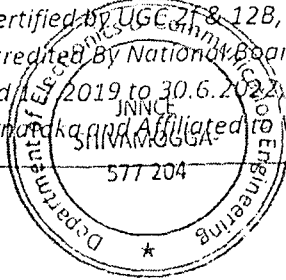| Sl.	Sem	Course	Course Name	Scrutinizer
1	4	18MAT41	Complex Analysis, Probability and Statistical Methods	Dr. Nirmala T
2	4	18MATDIP41	ADDITIONAL MATHEMATICS - II	Dr. Nirmala T
3	4	18EC42	Analog Circuits	Dr. Shetty Pramod Kumar
4	4	18EC43	Control Systems	Dr. Manjunath P
5	4	18EC44	Engineering Statistics & Linear Algebra	Dr. Sathyanarayana S V
6	4	18EC45	Signals & Systems	Dr. Manjunath P
7	4	18EC46	Microcontroller	Mrs. Ujwala B S
8	4	18CPC49	Constitution of India, Professional Ethics and Cyber Law	Mr. Sunil M D
9	6	17EC61	Digital Communication	Mr. Ajay Betur P
10	6	18EC61	Digital Communication	Mr. Ajay Betur P
11	6	17EC62	ARM Microcontroller & Embedded Systems	Dr. Sathyanarayana S V
12	6	18EC62	Embedded Systems	Dr. Sathyanarayana S V
13	6	17EC63	VLSI Design	Mr. Pradeepa S C
14	6	18EC63	Microwave & Antennas	Mr. Darshan K V
15	6	17EC64	Computer Communication Networks	Mrs. Ujwala B S
16	6	18EC643	Data Structures using C++	Mr. Anil Kumar J
17	6	18EC644	Digital System Design using Verilog	Mr. Sunil M D
18	6	18EC646	Python Application Programming	Mrs. Sheela S
19	6	17EC654	Digital Switching Systems	Mr. Anil Kumar J
20	6	17EC663	Digital System Design using Verilog	Mr. Sunil M D
21	8	17EC81	Wireless Cellular and LTE 4G Broadband	Mr. Ajay Betur P
22	8	17EC82	Fiber Optics & Networks	Mr. Ajay Betur P
23	8	17EC835	Network and Cyber Security	Dr. Sathyanarayana S V

National Education Society (R.)
J.N.N College of Engineering, Shivamogga
(Approved by AICTE, New Delhi, Certified by UGC 2f & 12B, Accredited by NAAC –'B',
All 7 UG Programs have been Accredited By National Board Of Accreditation (NBA)
for period 1.7.2019 to 30.6.2022,
Recognized by Govt. of Karnataka and Affiliated to VTU, Belagavi)

50	8B	17EC82	Fiber Optics & Networks	Mr. Anil Kumar J Mr. Prashanth G S
51	8A	17EC835	Network and Cyber Security	Mrs. Sheela S
52	8B	17EC835	Network and Cyber Security	Mr. Abhijeet N
53	8A	17EC84	Internship/Professional Practice	Mrs. Smitha S M
54	8B	17EC84	Internship/Professional Practice	Mr. Anil Kumar J
55	8A	17ECP85	Project Work	Mr. Sharath S M
56	8B	17ECP85	Project Work	Dr. Shetty Pramod Kumar
57	8A	17ECS86	Seminar	Mrs. Ujwala B S
58	8B	17ECS86	Seminar	Mrs. Nalina S B

K.V. Darshan

Signature of Time Table Co-ordinator

19.4

Signature of HOD

Head of the Department
Electronics and Communication
J.N.N. College of Engineering
SHIMOGA-577 204.
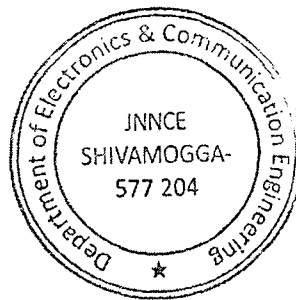
JNNCE
SHIVAMOGGA-
577 204

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Department of Electronics and Communication Engineering

Question Paper Scrutiny Committee

Academic Year: 2021-22(Odd)   Date: 09-03-2022

Sl.	Sem	Course	Course Name	Scrutinizer
1	3	18MAT31	Transform Calculus, Fourier Series and Numerical Techniques	Dr. Nirmala T
2	3	18EC32	Network Theory	Dr. Sathyanarayana S V
3	3	18EC33	Electronic Devices	Dr. Manjunath P
4	3	18EC34	Digital System Design	Dr. Sathyanarayana S V
5	3	18EC35	Computer Organization & Architecture	Mr. Ajay Betur P
6	3	18EC36	Power Electronics & Instrumentation	Dr. Shetty Pramod Kumar
7	3	18KAK39	Aadalitha Kannada (Kannada for Administration)	Dr. Sathyanarayana S V
8	3	18KBK39	Balake Kannada	
9	5	18ES51	Technological Innovation Management And Entrepreneurship	Mr. Ajay Betur P
10	5	18EC52	Digital Signal Processing	Dr. Manjunath P
11	5	18EC53	Principles of Communication Systems	Dr. Manjunath P
12	5	18EC54	Information Theory & Coding	Dr. Manjunath P
13	5	18EC55	Electromagnetic Waves	Mr. Darshan K V
14	5	18EC56	Verilog HDL	Mr. Sunil M D
15	5	18CIV59	Environmental Studies	
16	7	17EC71	Microwave and Antennas	Mr. Darshan K V
17	7	18EC71	Computer Networks	Mrs. Ujwala B S
18	7	17EC72	Digital Image Processing	Mrs. Sheela S
19	7	18EC72	VLSI Design	Mr. Sharath S M
20	7	17EC73	Power Electronics	Mrs. Shwetha B
21	7	18EC733	Digital Image Processing	Mrs. Sheela S
22	7	17EC744	Cryptography	Dr. Sathyanarayana S V
23	7	18EC741	IOT & Wireless Sensor Networks	Dr. Shetty Pramod Kumar
24	7	18EC744	Cryptography	Dr. Sathyanarayana S V
25	7	18EC745	Machine Learning with Python	Mrs. Sheela S
26	7	17EC752	IOT and Wireless Sensor Networks	Dr. Shetty Pramod Kumar
27	7	18EC754	Digital Systems Design using VHDL	Mr. Sunil M D

Signature of Program Co-ordinator
AJAY RETUR P
Asst. Professor, Dept. of ECE,
J N N College of Engineering
Shivamogga - 577 204.

Signature of HOD
Head of the Department
Electronics and Communication
JNNCE, SHIMOGA-577 204

Department of Electronics and Communication Engineering

Question Paper Scrutiny Committee

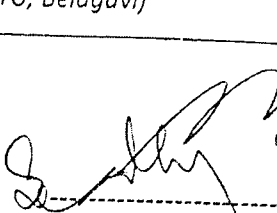**Academic Year: 2021-22(Even)**　　　　　　　　　　　　**Date: 10-05-2022**

Sl.	Sem	Course	Course Name	Scrutinizer
1	4	18MAT41	Complex Analysis, Probability and Statistical Methods	Dr. Nirmala T
2	4	18EC42	Analog Circuits	Dr. Shetty Pramod Kumar
3	4	18EC43	Control Systems	Mrs. Nalina S B
4	4	18EC44	Engineering Statistics & Linear Algebra	Dr. Sathyanarayana S V
5	4	18EC45	Signals & Systems	Dr. Manjunath P
6	4	18EC46	Microcontroller	Mrs. Ujwala B S
7	4	18CPC49	Constitution of India, Professional Ethics and Cyber Law	Mr. Sunil M D
8	6	18EC61	Digital Communication	Mr. Ajay Betur P
9	6	18EC62	Embedded Systems	Dr. Shetty Pramod Kumar
10	6	18EC63	Microwave & Antennas	Mr. Darshan K V
11	6	18EC643	Data Structures using C++	Mr. Anil Kumar J
12	6	18EC644	Digital System Design using Verilog	Mr. Sunil M D
13	6	18EC646	Python Application Programming	Mrs. Sheela S
14	8	17EC81	Wireless Cellular and LTE 4G Broadband	Mr. Ajay Betur P
15	8	18EC81	Wireless and Cellular Communication	Mr. Ajay Betur P
16	8	17EC82	Fiber Optics & Networks	Mr. Anil Kumar J
17	8	18EC821	Network Security	Dr. Sathyanarayana S V
18	8	18EC824	Optical Communication Networks	Mr. Anil Kumar J
19	8	17EC835	Network and Cyber Security	Dr. Sathyanarayana S V

Signature of Program Co-ordinator
AJAY BETUR P
Asst. Professor, Dept. of ECE,
J N N College of Engineering
Shivamogga - 577 204.

Signature of HOD
Head of the Department
Electronics and Communication
JNNCE, SHIMOGA-577 204

e. Calculation of CO attainment through the marks scored in CIE

```
        ( Begin )
            |
            v
+-----------------------------------+
| Course coordinator along with the |
| Faculty(s) handling the subject   |
| prepares Evaluation scheme &      |
| solution.                         |
+-----------------------------------+
            |
            v
+-----------------------------------+
| After the IA, faculties explain   |
| the solution scheme which enables |
| students to perform well in future|
+-----------------------------------+
            |
            v
+-----------------------------------+
| Evaluation of internal books      |
| adhering to scheme of evaluation  |
+-----------------------------------+
            |
            v
+-----------------------------------+
| Average of best two of three IAs  |
| is the final CIE Marks            |
+-----------------------------------+
            |
            v
+-----------------------------------+
| Calculation of CO attainment      |
| through the marks scored in CIE   |
+-----------------------------------+
            |
            v
         ( End )
```

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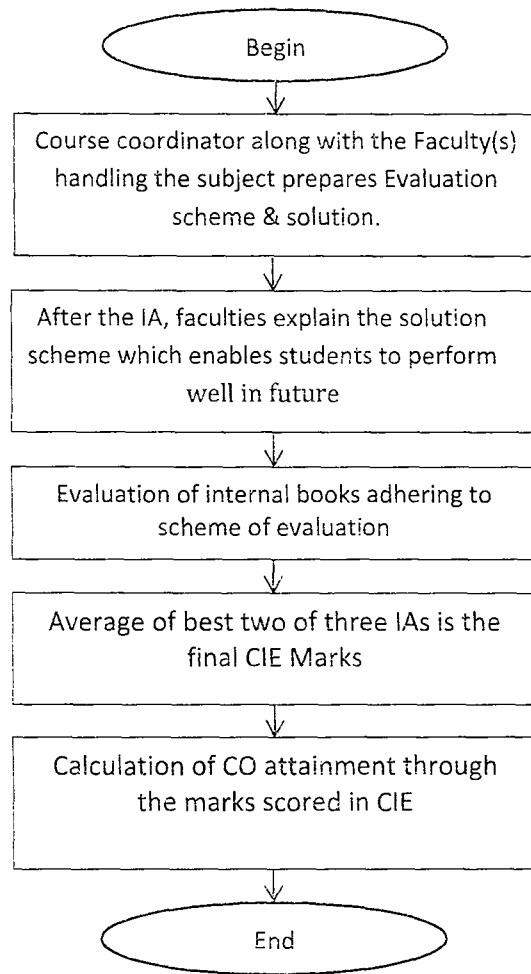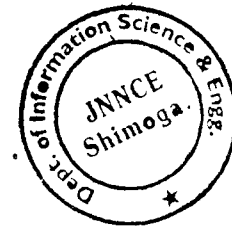
d. Observations from scrutiny committee are given to respective faculty and same has to be resubmitted after changes are done to scrutiny.

e. After approval from scrutiny committee QP has to be printed.

```
                        ┌─────────────┐
                        │    Start     │
                        └──────┬──────┘
                               ↓
              ┌────────────────────────────────┐
              │     Display of Test Time Table   │
              └────────────────┬───────────────┘
                               ↓
              ┌────────────────────────────────┐
              │  Faculty(s) handling the subject along with │
              │  course coordinator prepares the Question   │
              │                 Paper                        │
              └────────────────┬───────────────┘
                               ↓
              ┌────────────────────────────────┐
              │  Scrutiny Committee verifies the question   │
              │  paper for CO's, RBT and syllabus coverage  │
              └────────────────┬───────────────┘
                               ↓
                    ╱────────────────╲
          YES      ╱    If any         ╲
         ◄────────◄    suggestions?      ►
                    ╲                  ╱
                     ╲────────────────╱
                               ↓ NO
              ┌────────────────────────────────┐
              │  Faculty(s) handling the subject along with │
              │  course coordinator prepares the evaluation │
              │         scheme and solutions                 │
              └────────────────┬───────────────┘
                               ↓
                        ┌─────────────┐
                        │    Stop      │
                        └─────────────┘
```
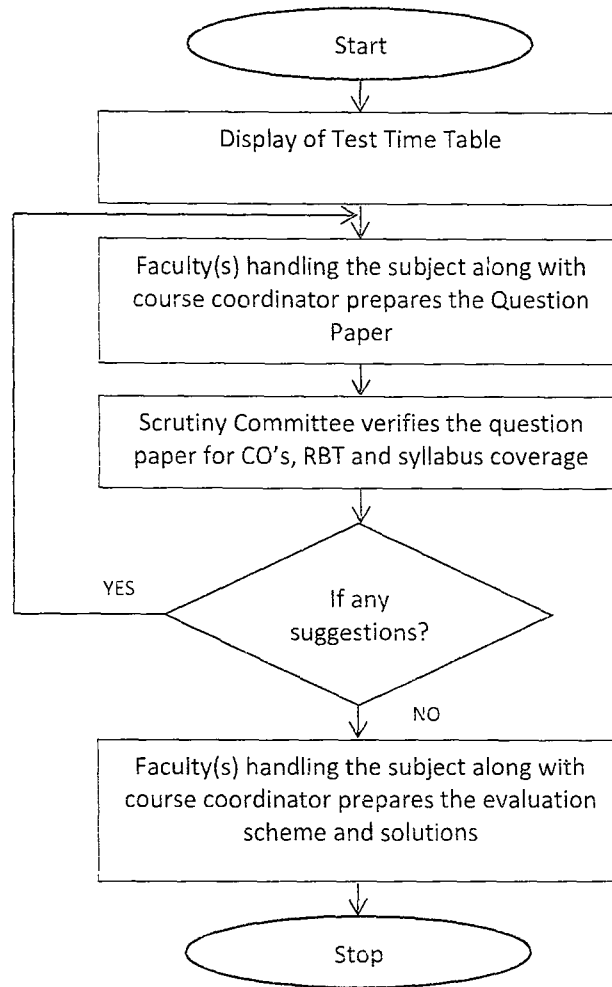
3. Process of Conduction of IA
    a. IA Coordinator issues a circular one week before IA to set the QP and get approval from Scrutiny Committee.
    b. Seating arrangements should be prepared and displayed on the day of IA. Seating arrangements should be changed for every course.
    c. After the IA, a circular is issued to enter the marks in OAM and display to the student's notice board.

4. Process of evaluation of IA
    a. Course coordinator along with the Faculty(s) handling the subject prepares Evaluation scheme & solution.
    b. After the IA, faculties explain the solution scheme which enables students to perform well in future
    c. Evaluation of internal books adhering to scheme of evaluation
    d. Final average marks should be entered in University portal and marks list to be get signed with the students.

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Department of Information Science and Engineering

Process for Internal Assessment Conductions & Evaluation

Department follows the following procedure to conduct the Continuous Internal Evaluation (CIE) tests.

1. Selection of IA Coordinators and their Roles & Responsibilities
   a. At the end of previous semester or before the beginning of current semester a meeting is called by HOD to assign the roles and responsibilities.
   b. Faculties interests are asked for IA Coordinator or Selected by HOD.
   c. Along with faculty, a technical support staff is chosen for the support.
   d. Question paper scrutiny committee is formed by using following process

Selection of Question paper Scrutiny Committee
a. Senior faculty among all the course handling faculties will be assigned as QP Scrutiny committee.
b. Same Faculty will be reviewing for one year (for both Odd and Even Semester).

Roles and Responsibilities of Question paper Scrutiny Committee
a. Review the question paper for
   i. Syllabus coverage
   ii. CO coverage
   iii. Accordance to Minutes of Meeting
   iv. Blooms Taxonomy Levels (BTLs)
   v. Quality of Questions
   vi. Marks Distribution

Roles and Responsibilities of IA Coordinator
a. Issuing of circulars about IA to students and faculties.
b. Preparing the IA Test Timetable.
c. Smooth conduction of IA

Roles and Responsibilities of IA Technical staff member
a. To prepare and display of seating arrangements
b. Smooth conduction of IA

2. Process for IA Test question paper setting
   a. College IA Coordinator issues Circular about IA dates and Schedule according to COE.
   b. Dept IA Coordinator Prepares Time Table and Circulate to students and faculty.
   c. Faculty(s) handling the subject along with course coordinator should prepare the Question Paper to be submitted to Scrutiny committee.